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CONFIRMATION NO. 1462

SERIAL NUMBER 10/642,416	FILING DATE 08/15/2003 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. 004192.P053D2
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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/865,929 05/24/2001 ABN
 which is a DIV of 09/286,946 04/08/1999 PAT 6,281,550 *yes n-h-l*

** FOREIGN APPLICATIONS *****

None n-h-l

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 04/06/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 9	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 2
Verified and Acknowledged	<i>[Signature]</i> Examiner's Signature Initials				

ADDRESS

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TITLE

Transistor and logic circuit on thin silicon-on-insulator wafers based on gate induced drain leakage currents

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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